

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application. No: 10/783,960

Filed: 2/20/2004

Inventor(s):

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Vishin, Sanjay

Examiner: Patrick Moore

Group/Art 2188

Unit:

Atty. Dkt. No: MIPS.0187-02-US

Title: Method and Apparatus for Global Ordering to Insure latency Independent Coherence

RESPONSE TO OFFICE ACTION OF
MAY 17, 2006

Mail Stop Amendment

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Dear Sir:

This paper is submitted in response to the Office Action of May 17, 2006, to further highlight why the application is in condition for allowance.